90mΩ, 2A Current Limited USB Power Switches

Features

- Input Voltage Range: 2.5V to 5.5V
- Fixed Current Limit
- Reverse Current Blocking
- Short-Circuit Response: 350ns
- Very Low Quiescent Current: 25μA (Typ)
- 1μA Max Shutdown Supply Current
- Under-Voltage Lockout
- Thermal Shutdown
- 4kV ESD Rating
- MSOP8 Packages
- Ambient Temperature Range: -40°C to +85°C

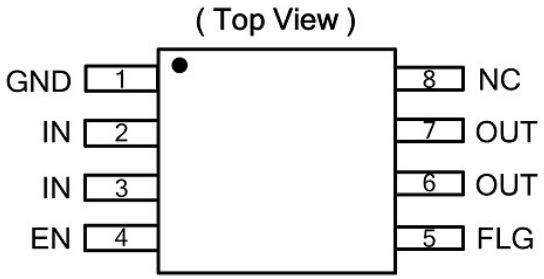
Applications

- Laptop/Desktop Computers and NetBooks
- 3G Wireless Cards
- Smart Phones and PDAs
- LCD TVs and Monitors
- Set-Top-Boxes
- MP3/MP4
- Printers
- Portable Game Players
- Portable Media Players and MIDs
- USB Keyboards
- USB Hard Disk Drives
- USB Memory Drives
- USB Hubs

General Description

The FS9005 are current limited P-channel MOSFET power switch designed for high-side load switching applications. This switch operates with inputs ranging from 2.5V to 5.5V, making it ideal for both 3.3V and 5V systems. An integrated current-limiting circuit protects the input supply against large currents which may cause the supply to fall out of regulation. The FS9005 is also protected from thermal overload which limits power dissipation and junction temperatures. Current limit threshold is fixed internally. The quiescent supply current in active mode is only 25μA. In shutdown mode, the supply current decreases to less than 1μA. The FS9005 is available in Pb-free packages and is specified over the -40°C to +85°C ambient temperature range.

Pin Configurations



Ordering Information

PART NUMBER	TEMPERATURE RANGE	MAXIMUM CURRENT	PACKAGE	TAPE&REEL
FS9005	-40 C to 85 C	2A	MSOP8	-T

Available Options of the PowerSiliconTech USB Power Switches

PART NUMBER	CH	ENABLE	$R_{DS(ON)}$	Current Limit	MAX IOUT (DC)	PACKAGES
FS9005	1	Active High	90mΩ	2.2A	2A	MSOP8

Pin Description

Pin No.	Pin Name	Pin Function
1	GND	Ground Pin
2,3	IN	Power supply input
4	EN	Enable input
5	$\overline{FLT}$	Overcurrent and over-temperature fault reporting signal, output, active low with 4.5ms blanking time for overcurrent, conditions and 0ms blanking for over-temperature conditions.
6,7,8	OUT	Power output.

Absolute Maximum Rating

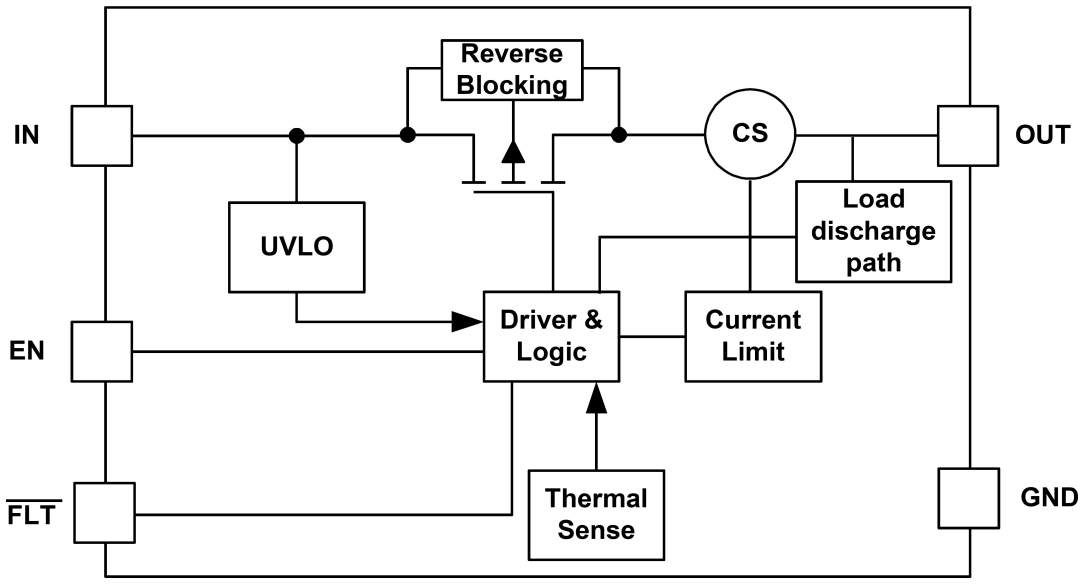
Parameter	Rating	Unit
IN, EN, /FLT Voltage	-0.3 to 6	V
OUT Voltage	-0.3 to V_{IN} + 0.3	V
OUT Current	Internal Limited	A
Power Dissipation	2	W
Package Thermal Resistance(θ_{JA})	160	°C/W
Operating Junction Temperature	-40 to 125	°C
Storage Temperature	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)	300	°C

Electrical Characteristics

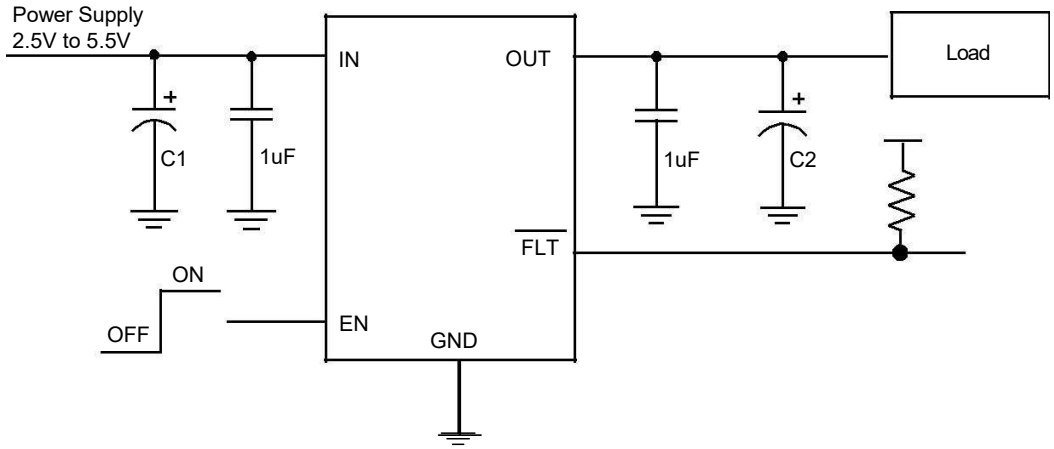
(V_{IN} = +5.0V, T_A = -40°C to 85°C, typical values at T_A=25°C, unless otherwise stated)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
V_{IN}	Input Voltage Range		2.7		5.5	V
V_{UVLO}	Input UVLO		1.8		2.5	V
I_{SHDN}	Input Shutdown Quiescent Current	Disabled, V_{EN}=0V, OUT floating or shorted to ground		0.1	1	uA
I_Q	Input Quiescent Current /Channel	Enabled, V_{EN}=V_{IN}, I_{OUT}= 0		25	40	uA
$R_{DS(ON)}$	Switch on-resistance	V_{IN}=5V, I_{OUT}=1.3A		90	110	mΩ
I_{LMT}	Current Limit	V_{IN}=5V, V_{OUT}=4.5V	2.2			A
V_{IL}	EN Input Logic Low Voltage				0.8	V
V_{IH}	EN Input Logic High Voltage		2.0			V
I_{SINK}	EN Input leakage	V_{EN} = 5V		0.01	1	uA
$T_{D(ON)}$	Output Turn-on Delay Time	V_{IN} =5V, C_L=1uF, R_{load}=10		10		us
T_R	Output Turn-on Rise Time	V_{IN} =5V, C_L=1uF, R_{load}=10		800		us
$T_{D(OFF)}$	Output Turn-off Delay Time	V_{IN} =5V, C_L=1uF, R_{load}=10		60		us
T_F	Output Turn-off Fall Time	V_{IN} =5V, C_L=1uF, R_{load}=10		20	200	us
T_{FLT_BLANK}	FLT Blanking Time			4		ms
V_{FLT_Lo}	FLT Logic Low Voltage	IFLT(SINK) =1mA			0.4	V
I_{FLT}	FLT Leakage Current	V_{FLT} =5V, Enabeld, No Conditions		0.1	1	uA
$R_{dischrg}$	Output discharge FET Rdson	V_{IN} = 5V, EN=0V, VOUT=5V		100	200	Ω
T_{SHDN}	Thermal shutdown threshold	V_{IN} = 5V		135		℃
T_{HYS}	Thermal shutdown hysteresis	V_{IN} = 5V		15		℃

Typical Block Diagram

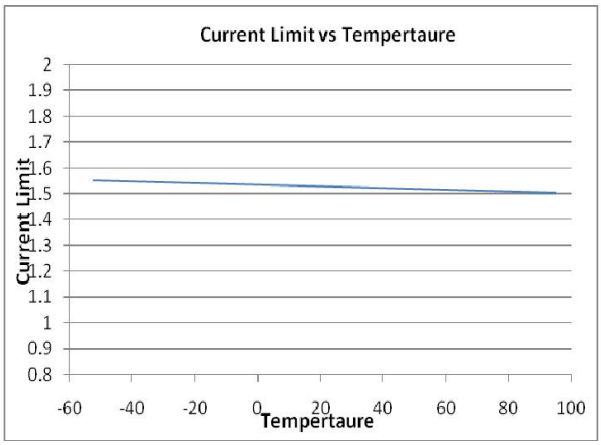


- **Typical Application Diagram**

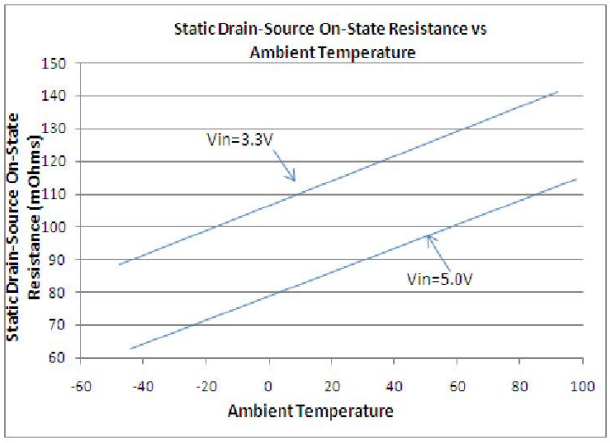


- **Typical Performance Characteristics**

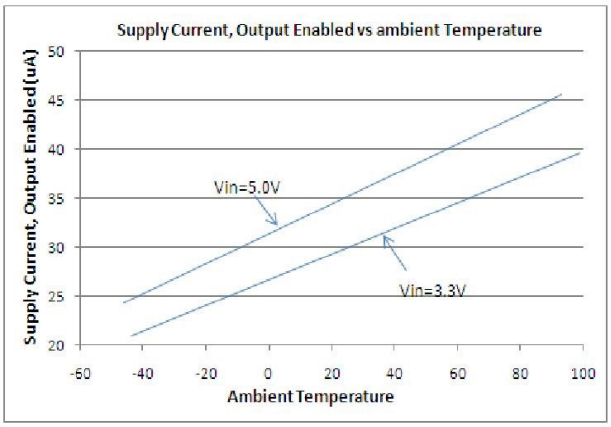
Current Limit VS Temperature



RDS(on) VS Temperature



Supply Current VS Temperature



Current Limit VS Input Voltage

Operation

FS9005 is an integrated power switch with a low R_{dson} P-channel MOSFET, internal gate rive circuit, programmable current limiting, and thermal protection. When the device is active, if there is no load, the device only consumes 25uA supply current, which makes the device suitable for battery powered applications.

Power Supply Considerations

A 0.01-μF to 0.1-μF ceramic bypass capacitor between IN and GND, close to the device, is recommended.

Placing a high-value electrolytic capacitor on the output pin(s) is recommended when the output load is heavy. This precaution reduces power-supply transients that may cause ringing on the input and minimize the input voltage droops. Additionally, bypassing the output with a 0.01-μF to 0.1-μF ceramic capacitor improves the immunity of the device to short-circuit transients.

Power Dissipation and Junction Temperature

The low on-resistance on the P-channel MOSFET allows the small surface-mount packages to pass large currents. It is good design practice to check power dissipation and junction temperature for each application. Begin by determining the $R_{DS(ON)}$ of the P-channel MOSFET relative to the input voltage and operating temperature. Using the highest

operating ambient temperature of interest and $R_{DS(ON)}$, the power dissipation per switch can be calculated by:

$$P_D = R_{DS(ON)} \times I^2$$

Finally, calculate the junction temperature:

$$T_J = P_D \times R_{\Theta JA} + T_A$$

Where:

T_A= Ambient temperature

$R_{\Theta JA}$ = Thermal resistance

P_D = Total power dissipation

Compare the calculated junction temperature with the maximum junction temperature which is 125℃. If they are within degrees, either the maximum load current needs to be reduced or another package option will be required.

FLT Output

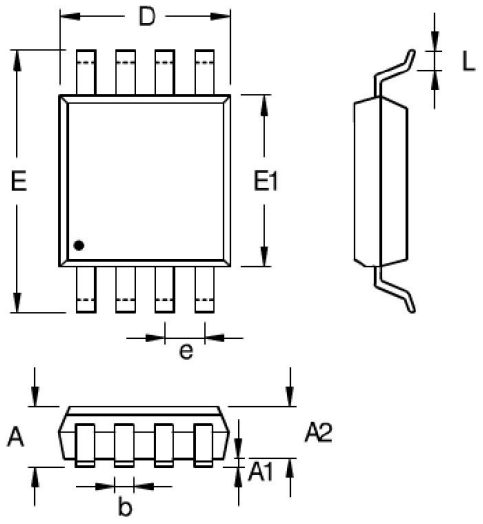
The FAULT Flag (FLT) is provided to alert the system if a FS9005 load is not receiving sufficient voltage to operate properly. If current limiting circuit is active for more than approximately 4ms, the FAULT Flag is pulled to ground through an approximately 100Ω resistor. The filtering of voltage or current transients of less than 4ms prevents capacitive loads connected to the FS9005 output from activating the FAULT Flag when they are initially attached. However, if the device is entering over-temperature conditions, the FLT will be pulled low without delay or deglitch. Pull-up resistance of 1kΩ to 100kΩ on FLT pin is recommended. Since FLT is an open drain terminal, it may be pulled up to any unrelated voltage less than the maximum operating voltage of 5.5V, allowing for level shifting between circuits.

Thermal Protection

Thermal protection prevents damage to the IC when heavy-overload or short-circuit faults are present for extended periods of time. The FS9005 implements a thermal sensing to monitor the operating junction temperature of the power distribution switch. In an overcurrent or short-circuit condition, the junction temperature rises due to excessive power dissipation. Once the die temperature rises to approximately 135°C due to overcurrent conditions, the internal thermal sense circuitry turns the power switch off, thus preventing the power switch from damage. Hysteresis is built into the thermal sense circuit, and after the device has cooled approximately 15°C, the switch turns back on. The switch continues to cycle in this manner until the load fault or input power is removed.

- **Package Information**

MSOP8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.810	1.100	0.032	0.043
A1	0.000	0.150	0.000	0.006
A2	0.750	0.950	0.030	0.037
b	0.220	0.380	0.009	0.015
D	2.900	3.100	0.114	0.122
e	0.650		0.026	
E	4.800	5.000	0.189	0.197
E1	2.900	3.100	0.114	0.122
L	0.400	0.800	0.016	0.031